

**TOWARDS A POWER CONSUMPTION ESTIMATION MODEL FOR
ROUTERS OVER TCP AND UDP PROTOCOLS**

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**SCHOOL OF COMPUTING
UUM COLLEGE OF ARTS AND SCIENCES
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A dissertation submitted to Dean of Awang Had Salleh Graduate School in
Partial Fulfilment of the requirement for the degree
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By

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Abstract

Due to the growing development in the information and communication technology (ICT) industry, the usage of routers has increased rapidly. Meanwhile, these devices that are produced and developed today consume a definite amount of power, Furthermore, with limited focus on power estimation techniques and the increased demands of networking devices, it led to an increase of the vitality consumption as a result. While new high capacity router components are installed, energy intake in system elements will be rising due to the higher capability network consuming larger component of the vitality. This study considers providing estimating power model in different traffic settings over TCP and UDP protocols, this study is mainly concerned about the transport protocols power consumption. Isolating the power consuming components within an electronic system is a very precise process that requires deep understanding of the role of each component within the system and a thorough study of the component datasheet. The study started by simulating the protocols mechanism then followed by protoclos power measurements, a simple simulation has been provided for Xilinx Virtex-5, it is very complicated to simulate the whole system due to the need of an external devices, so the simulation focused on wavelengths, fequencies and traficc types. This study found that the estimated power stokes was high when the 1480nm, 1580nm, and 1750nm power source increase.while there were diferrence in the consumed power while trasiting different types of traffic such as CBR and HTTP throug UDP and TCP. The effect of different frequencies has been noticed also while applying different frequencies to the protocols. So it is believed that this study may enhance the power scenarios in the netwrok and routers throug applying different techniques to UDP and TCP.

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Ali Qusay Mohammed

UUM, Sintok, June, 2015

Dedicated to the soul of my wife

To my parents, my sisters and my son

A handwritten signature in black ink, appearing to be 'Ali Qusay', with a stylized, somewhat abstract flourish.

Ali Qusay

25th June 2015

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List of Abbreviations

ARP	Adress Resolution Protocol
ARPNET	Advanced Research Projects Agency Network
CBR	Constant Bit Rate
DC	Direct Current
DVFS	Dynamic Voltage Frequency Scaling
FPGA	Field Programmable Gate Array
HDL	Hardware Description Language
HTTP	Hyper Text Transfer Protocol
ICT	Information and Communications Technology
IP	Internet Protocol
IT	Information Technology
LAN	Local Area Network
NOC	Network On Chip

RF	Radio Frequency
RPAR	Real-time Power Aware Routing
SSL	Secure Socket Layer
TCP	Transmission Control Protocol
UDP	User Datagram Protocol
VHSIC	Very High Speed Integrated Circuit
WLAN	Wireless Local Area Network

CHAPTER ONE

INTRODUCTION

1.1 Introduction

Energy consumption and estimation are important aspects that drive the technology development nowadays (Sen, 2012; Trivailo, Sippel, & Şekercioğlu, 2012). With the evolution in computer hardware design and manufacturing, the circuit demand for energy has continuously risen and is also crucial for the continued development (Pocek, Tessier, & DeHon, 2013). This was reasoned by some researcher as the need for extending the circuit capacity due to heavy traffic demands which in turn result in more power consumption (Aymerich & Rubio, 2013; McGinnis, 2014). On the other hand, almost all router devices that are produced and developed today consume a definite amount of power (Kirschbaum & Plos, 2014; C. A. Lee, Gasster, Plaza, Chang, & Huang, 2011). Due to the growing development in the information and communication technology (ICT) industry, the usage of these devices has increased rapidly. Consequently, power is being consumed at an ever increasing rate (C. A. Lee et al., 2011). Unfortunately, this trend is also producing adverse effects on circuit capability to effectively estimate power consumption and also result to undesired heating effects. Thus, power consumption estimation techniques can provide the alternative the opportunity to balance power consumption in routers.

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